

**Processing Engine** 

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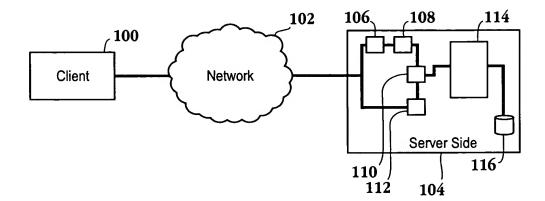


Fig. 1 (prior art)

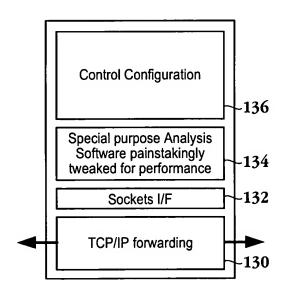


Fig. 2 (prior art)

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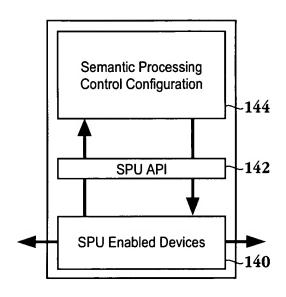


Fig. 3

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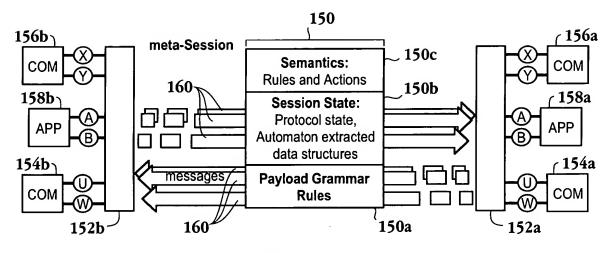


Fig. 4A

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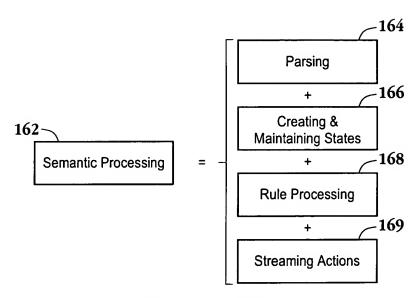


Fig. 4B

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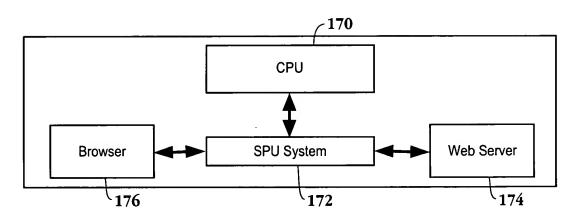
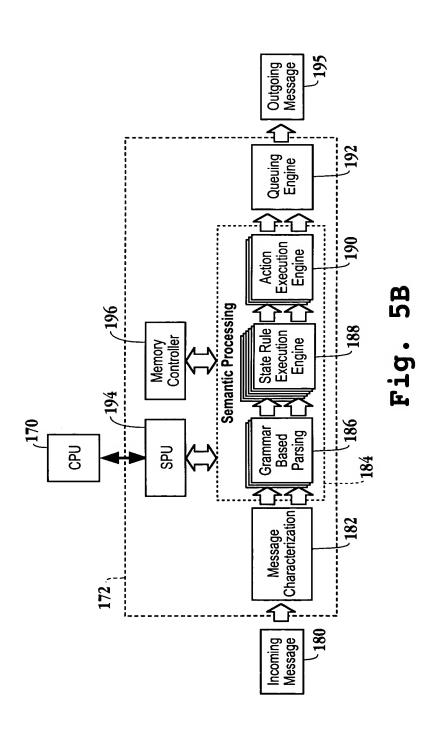


Fig. 5A

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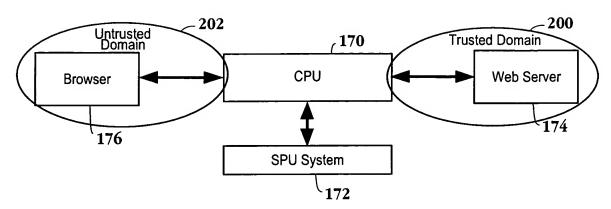


Fig. 6A

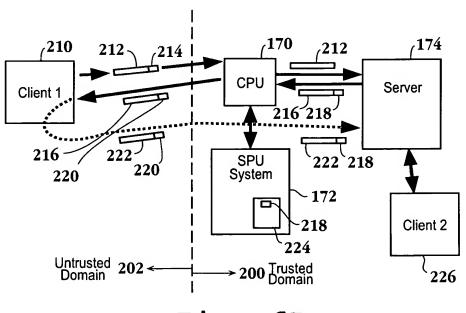


Fig. 6B

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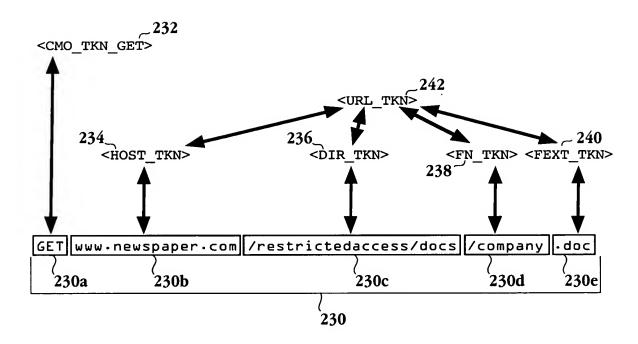


Fig. 7

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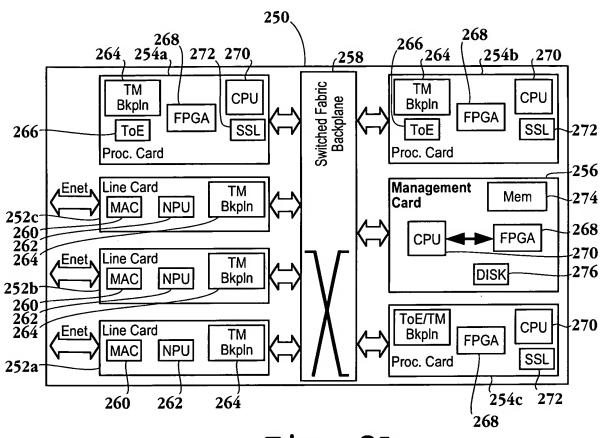


Fig. 8A

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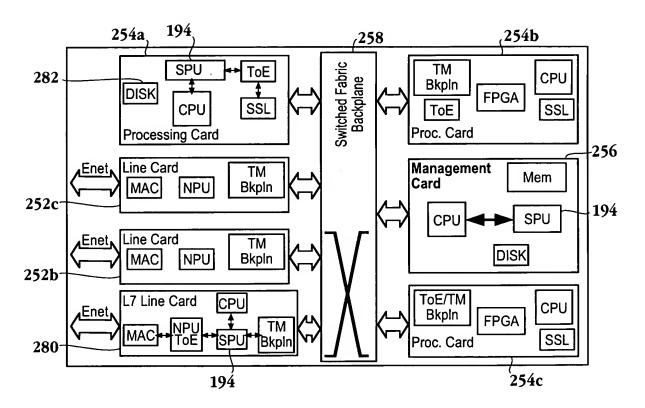
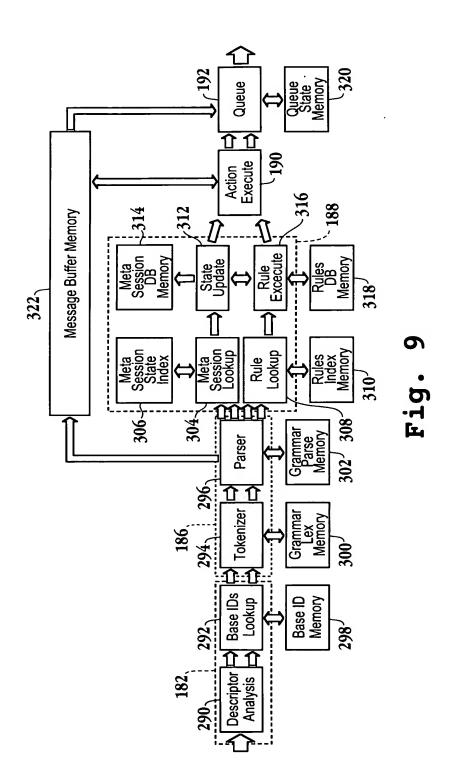


Fig. 8B

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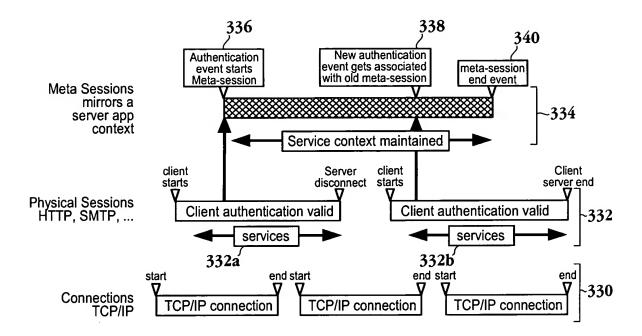


Fig. 10

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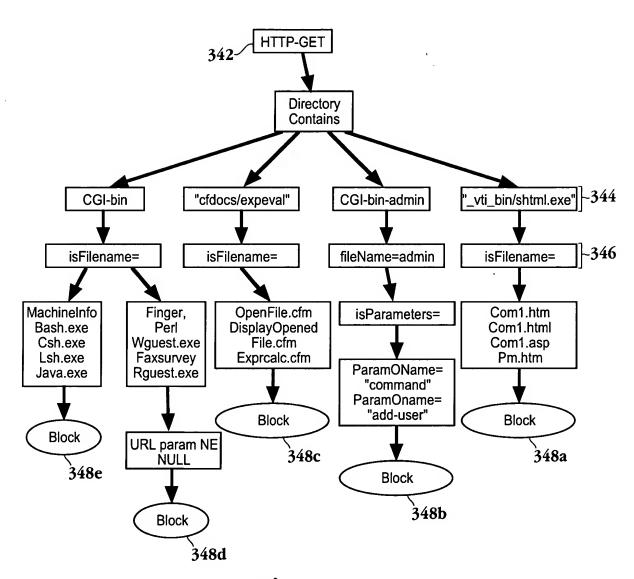
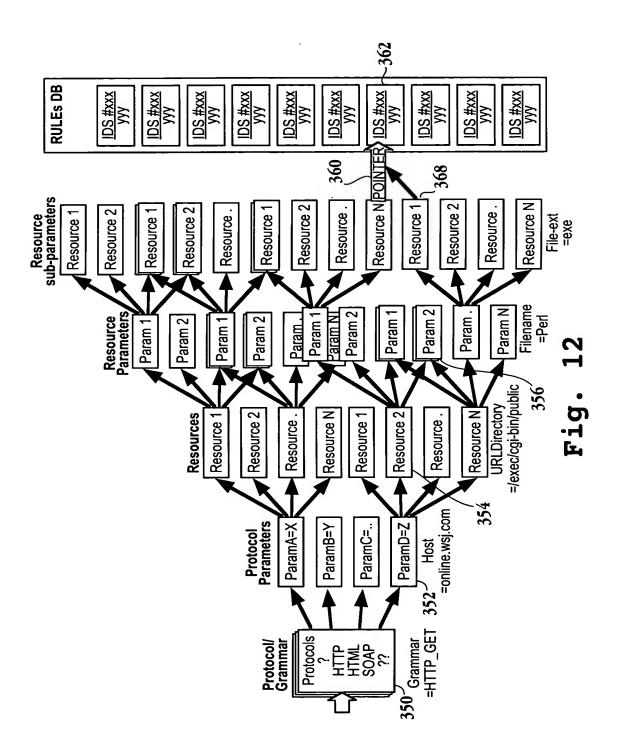


Fig. 11

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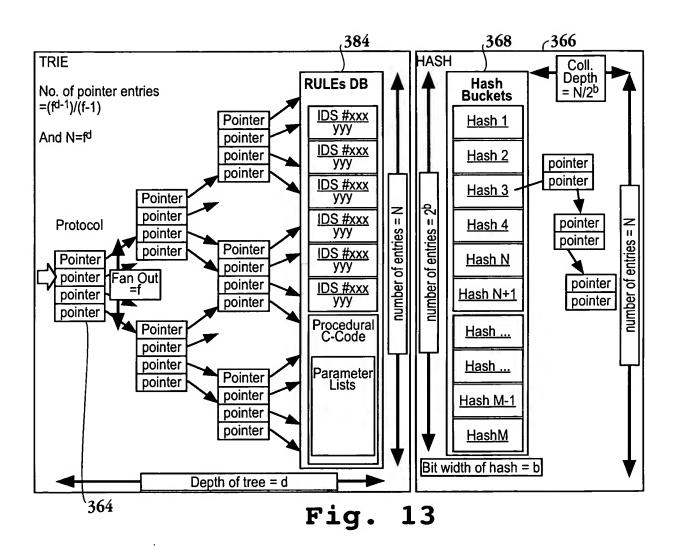
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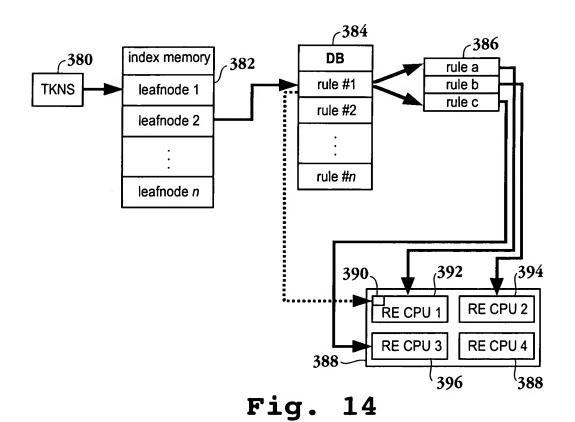
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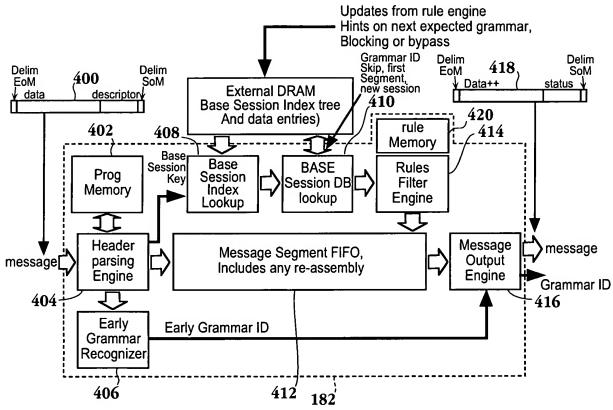


Fig. 15

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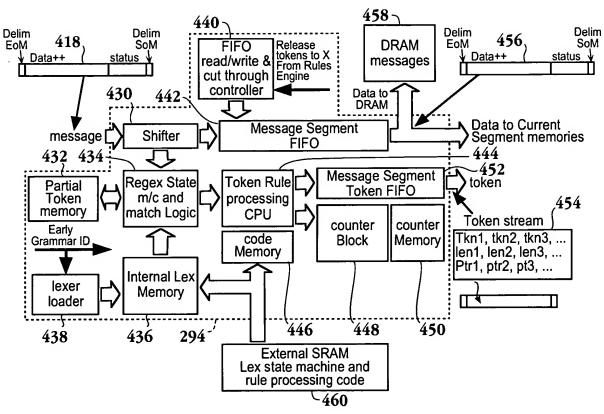


Fig. 16

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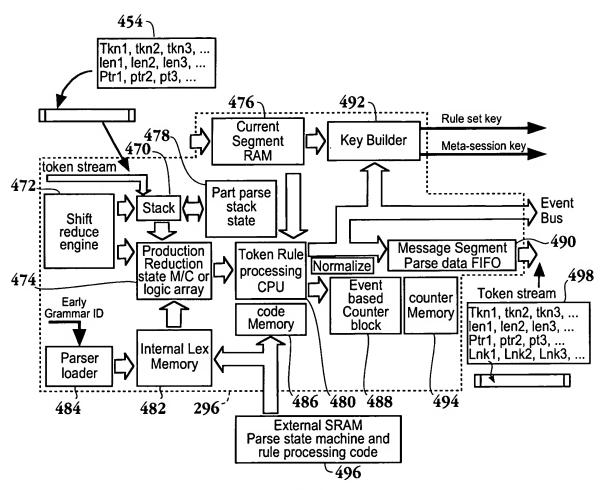
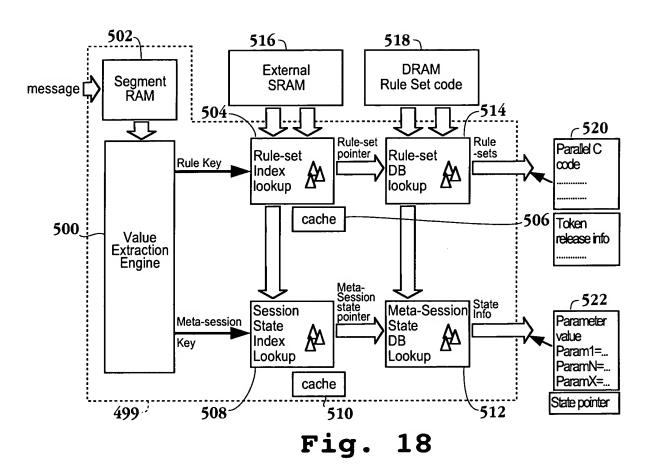


Fig. 17

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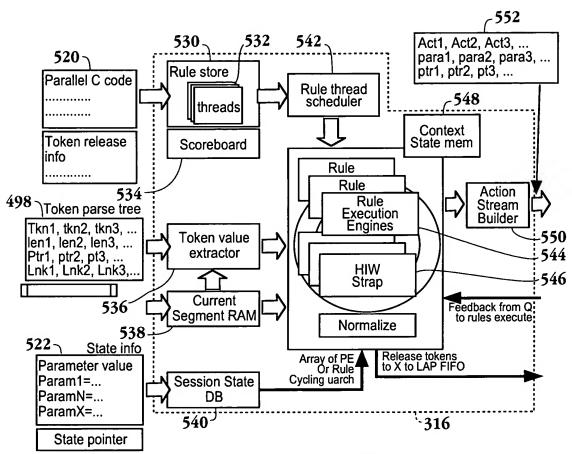
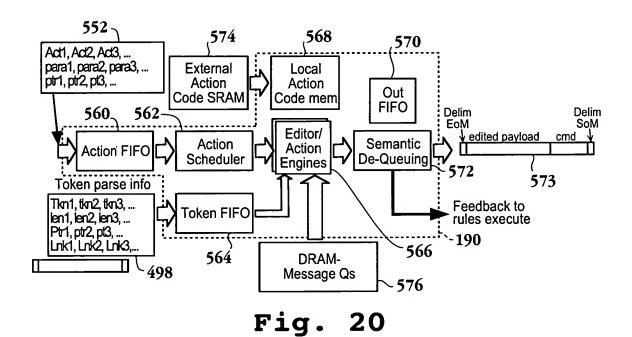


Fig. 19

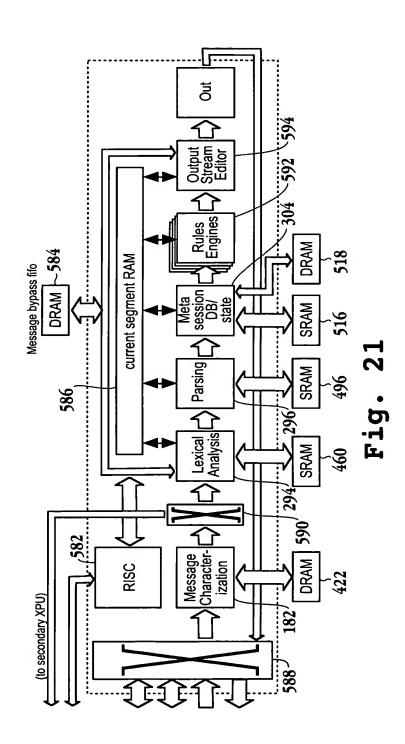
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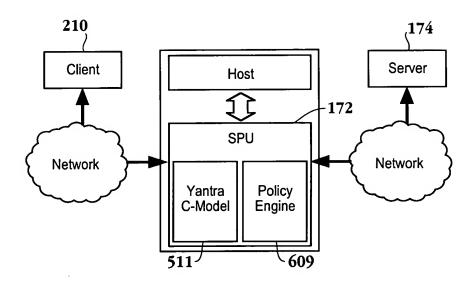


Fig. 22A

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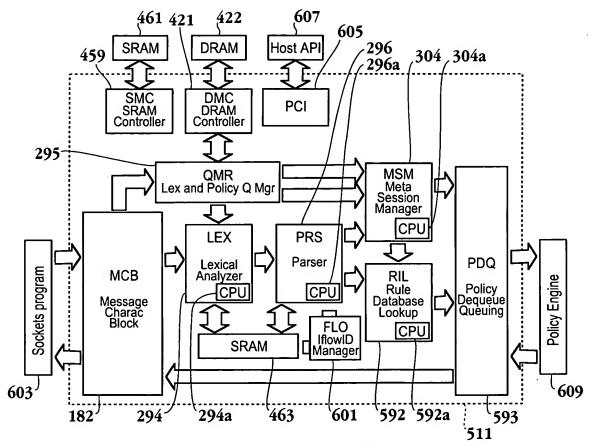


Fig. 22B

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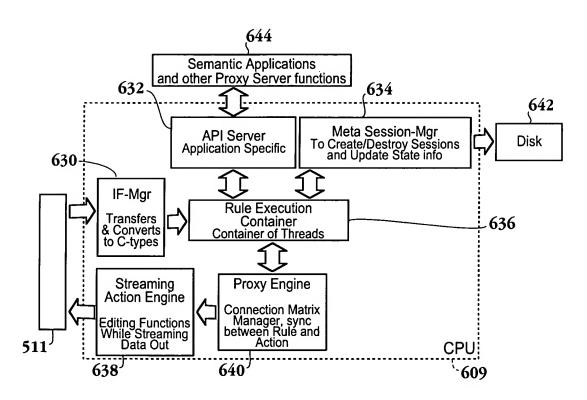
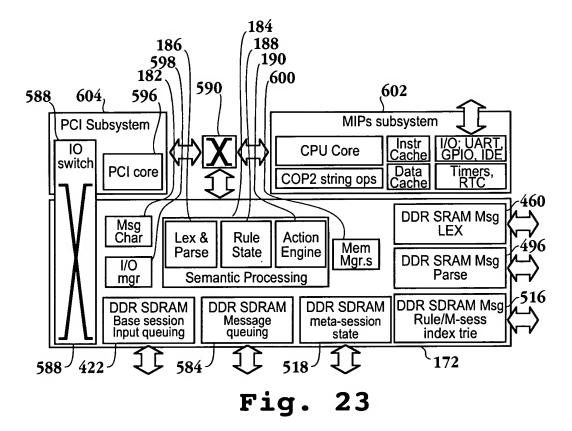


Fig. 22C

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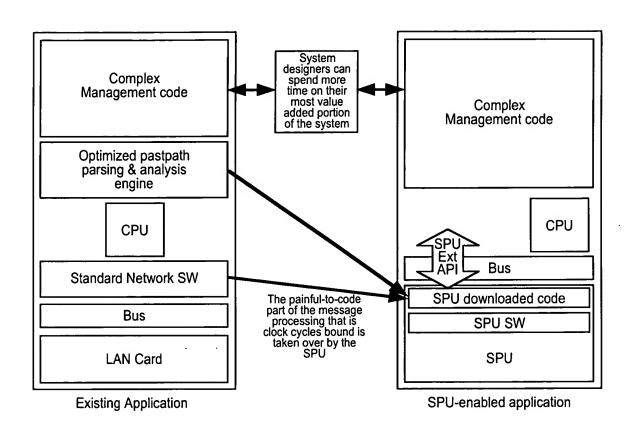


Fig. 24

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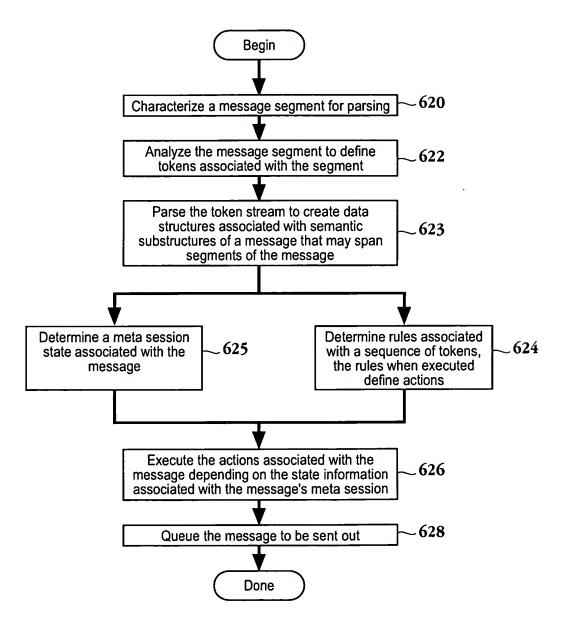


Fig. 25